

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	§	Patent No.:	6,974,333
Douglas Glenn Wildes et al.	§	Issued: December 13, 2005	
	§		
Application No.:	§	Group Art Unit.:	2839
	§		
Filed:	§	Confirmation No.:	5777
	§		
For: HIGH DENSITY CONNECTION	§	Attorney Docket:	GERD:0556/YOD
BETWEEN MULTIPLE CIRCUIT	§		134730
BOARDS	§		

Commissioner for Patents
 Certificate of Correction Branch
 P.O. Box 1450
 Alexandria, VA 22313-1450

CERTIFICATE OF TRANSMISSION OR MAILING 37 C.F.R. 1.8	
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July 14, 2009 Date	/Patrick S. Yoder/ Patrick S. Yoder

REQUEST FOR CERTIFICATE OF CORRECTION UNDER RULE 322

In accordance with the provisions of Rule 322 of the Rules of Practice, the Commissioner is respectfully requested to issue a Certification of Correction in the above-identified patent to reflect the corrections shown in the attached Certificate of Correction Form PTO/SB/44.

Since the vast majority of these corrections were occasioned because of Patent Office printing errors, Applicant requests the Patent Office to issue a Certificate of Correction or to place a copy of this Certification of Correction in the file.

When the above requested Certificate of Correction has been completed and printed, please forward the official Certificate of Correction to the undersigned for attachment to the original Letters Patent.

Respectfully submitted,

Dated: July 14, 2009

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,974,333

APPLICATION NO.: 10/813,945

ISSUE DATE : December 13, 2005

INVENTOR(S) : Douglas Glenn Wildes, Robert Stephen Lewandowski, and Geir Ultveit Haugen

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claims 20 and 21 as they now read should be deleted in their entirety.

Claims 20 and 21 should read as follows:

--20. An apparatus comprising:

first, second and third stacked connections, a first spacer disposed between said first and second stacked connections, and a second spacer disposed between said second and third stacked connections, each of said first, second and third stacked connections comprising a respective plurality of circuit boards, wherein said second spacer comprises a hole and each circuit board of said second stacked connection comprises a respective opening, further comprising a first alignment pin anchored in said first spacer and projecting through said openings in said circuit boards of said second stacked connection and into said hole in said second spacer; and
a clamp that clamps said first through third stacked connections and said first and second spacers together, said clamp comprising a bolt, wherein each of said first through third stacked connections has an opening penetrated by said bolt, and each of said first and second spacers has a hole penetrated by said bolt.

21. The apparatus as recited in claim 20, further comprising:

a fourth stacked connection comprising a plurality of circuit boards, and
a third spacer disposed between said third and fourth stacked connections, wherein said third spacer comprises a hole and each circuit board of said third stacked connection comprises a respective opening, further comprising a second alignment pin anchored in said second spacer and projecting through said openings in said circuit boards of said third stacked connection and into said hole in said third spacer.--

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